In response to that Office Action, please amend the above-identified application as follows:

IN THE CLAIMS

Please cancel Claim 70-72 and 101 without prejudice or disclaimer of subject matter.

Please amend Claims 69, 73, and 85, and add Claims 104-106, to read as follows.¹

69. (Twice Amended) A display apparatus comprising:

an electron source plate, having a substrate and a plurality of electron-emitting devices arranged in a matrix of rows and columns on the substrate, said electron source plate also comprising a matrix configuration of a plurality of row wires and N column wires respectively corresponding to the rows and columns of the electron-emitting devices arranged in the matrix, each of said N column wires being connected exclusively to a corresponding one of N column leads;

a fluorescent device plate having a fluorescent layer and an acceleration electrode;

The claims amended herein are shown completely underlined, since they were previously added in this reissue application (see, e.g., MPEP § 1453). Applicants understand that it is not necessary to include a marked-up version of the amended claims on any separate pages, since this is a reissue application (see, e.g., 37 C.F.R. §§ 1.121(h) and 1.173(b)).

a housing having a structure adapted for maintaining a vacuumized condition in a space between said electron source plate and said fluorescent device plate, at least a portion of said structure being formed by said electron source plate and said fluorescent device plate; and

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a voltage applier disposed outside of the housing, and arranged for applying (1) a scan signal to the row wires, (2) a modulation signal to the column wires, and (3) an acceleration voltage to the acceleration electrode to accelerate electrons emitted from the electron-emitting devices toward the fluorescent layer of said fluorescent device plate, the modulation signal comprising a series of one-row data of image data which is to be assigned to the N column wires and each one-row data of image data in the series being sequentially applied one-row data by one-row data to the N column leads in synchronization with the scan signal.

73. (Twice Amended) A display apparatus comprising:

an electron source plate, having a substrate and a plurality of electron-emitting devices arranged in a matrix of rows and columns on the substrate, said electron source plate also comprising a matrix configuration of a plurality of row wires and N column wires respectively corresponding to the rows and columns of the electron-emitting devices arranged in the matrix, each of said N column wires being connected exclusively to a corresponding one of N column leads;

a fluorescent device plate comprising a laminated layer having a fluorescent layer and an acceleration electrode;

a housing having a structure adapted for maintaining a vacuumized condition in a space between said electron source plate and said fluorescent device plate, at least a portion of said structure being formed by said electron source plate and said fluorescent device plate; and

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a voltage applier disposed outside of the housing, and arranged for applying (1) a scan signal to the row wires, (2) a modulation signal to the column wires, and (3) an acceleration voltage to the acceleration electrode to accelerate electrons emitted from the electron-emitting devices toward the fluorescent layer of said fluorescent device plate, the modulation signal comprising a series of one-row data of image data which is to be assigned to the N column wires and each one-row data of image data in the series being sequentially applied one-row data by one-row data to the N column leads in synchronization with the scan signal.

85. (Amended) A display apparatus comprising:

an electron source plate, having a substrate and a plurality of electron-emitting devices arranged in a matrix of rows and columns on the substrate, said electron source plate also comprising a matrix configuration of a plurality of row wires and N column wires respectively corresponding to the rows and columns of the electron-emitting devices arranged in the matrix, each of said N column wires being connected exclusively to a corresponding one of N column leads;

a fluorescent device plate comprising a laminated layer having a fluorescent layer and an acceleration electrode;

a housing having a structure adapted for maintaining a vacuumized condition in a space between said electron source plate and said fluorescent device plate, at least a portion of said structure being formed by said electron source plate and said fluorescent device plate; and

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leads extending from inside of said housing to outside of said housing, and arranged for applying (1) a scan signal to the row wires, (2) a modulation signal to the column wires, and (3) an acceleration voltage to the acceleration electrode to accelerate electrons emitted from the electron-emitting devices toward the fluorescent layer of said fluorescent device plate, the modulation signal comprising a series of one-row data of image data which is to be assigned to the N column wires and each one-row data of image data in the series being sequentially applied one-row data by one-row data to the N column leads in synchronization with the scan signal.

104. (New) The display apparatus of Claim 73, wherein the modulation signal is an information signal.

105. (New) The display apparatus of Claim 73, wherein the modulation signal is applied simultaneously to scanned ones of the electron-emitting devices in synchronization with the scan signal.

106. (New) The display apparatus of Claim 73, wherein said fluorescent device plate comprises red, green, and blue fluorescent members.